

SOFTWARE

**Operating System and Applications** 

duction sites that require a reli-

point restart or a robust batch en-

vironment, are available today.

With traditional NEC reliabil-

ity and the SUPER-UX operating made easy through the simplicity

system, now in its 12th year, the of vector programming and the SX-6 series is designed for pro- support of NEC's optimizing com-

able platform. All the functions Development Environment proother systems promise, like check-vides all of the tools and utilities

All third party applications rele- compiling, testing, debugging vant to parallel vector supercom- and performance optimization. puters are available and provide The cross development environ-

industry-leading performance. ment PSUITE is available on all

Turn around time for solutions popular workstation class prodis minimized. Complex analysis ucts as well as Linux personal

streams can be completed over- computers to maximize accessibilnight instead of taking a week. All ity and development efficiency.

application software available for The languages, libraries and tools

the larger SX models is also avail- available include Fortran90, For-

able on the SX-6i. This includes tran95, C and C++.

MATLAR

# 

Applications development is

pilers. NEC's PSUITE Integrated

necessary under a single package

for project management, editing,

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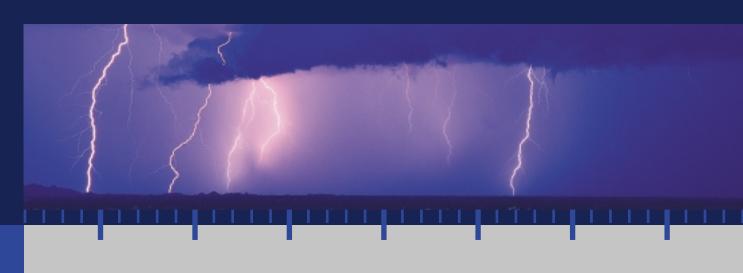
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# **THE NEC SX-6i**

## **DESK-SIDE SUPERCOMPUTER WITH SINGLE-CHIP VECTOR PROCESSOR**



# **VECTOR SUPERCOMPUTING: RIGHT BESIDE YOUR DESK**

"A vector supercomputer that you can utilize freely in your laboratory in your department, just like your personal computer." This is the main concept of the NEC SX-6i, the smallest member of the NEC SX-6 series. With a choice of desk-side or rackmountable chassis, the SX-6i is the supercomputer that fits right next to your desk or in every departmental computer room.

Vector supercomputers have always provided the absolute highest performance available, with high-performance high-bandwidth memory, powerful processors and commercial robustness for production sites.

NEC has now substantially lowered the entry level to this technology with the introduction of the cost effective SX-6i server. With attractive pricing and flexible in-

# **CONFIGURATION TABLE**

applications for vehicle crash analysis, computational fluid dy-

namics, structural analysis and

others.

Hardware			Operating			
Processor	CPU	One NEC single-chip vector processor	Conditions			
	Peak performance	8 GLFOPS	Floor layout	Size	Desk-side	445(W) x 730(D) x 700(H) mm
Main Memory	Capacity	4GB or 8GB	1		Rack mount (25U)	600(W) x 1000(D) x 1265(H) mm
	Bandwidth	32 GB/sec	1		Rack mount (37U)	600(W) x 1000(D) x 1800(H) mm
I/O interface	Max. channels	Max. 6 channels inclunding system channel	1	Weight	100 kg desk-side,	310 kg 25U rack,
	Interfaces	Gigabit Ethernet, Ethernet, FC-AL, SCSI,	]		410 kg 37U rack	
			Power	AC Input	Single phase 100 V or single phase 200 V 2 kVA	
Software				Consumption		
Operating	Standard Unix OS	NEC SUPER-UX (64 bit)	]	Heat dissipation	7100 kJ/h	
System			Climate	Temperature	5~38 °C (recomm	ended are 16~28 °C
Languages	FORTRAN	FORTRAN90/SX (FORTRAN95)	1		for cooling air ent	ering the chassis)
	C/C++	C/SX and C++/SX	1	Humidity	10~80%	
Operation	Batch system	NQS				
	Debugger	dbx, xdbx	11			

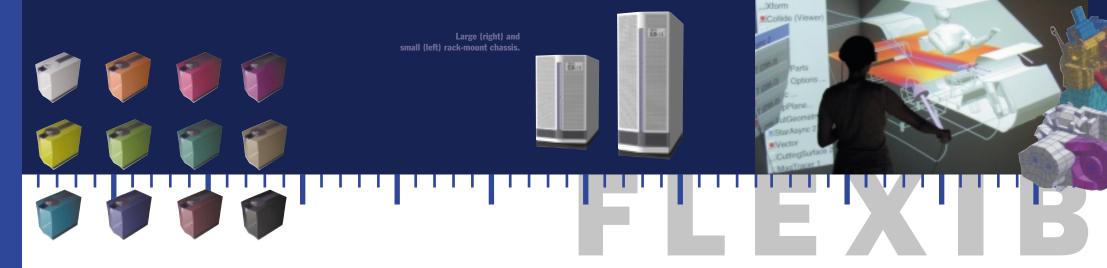
Empowered by Innovation







stallation requirements, the combination of world's fastest single chip processor and its unmatched high performance memory subsystem is now accessible to a much wider audience. Its superior UNIX-based development environment makes it an outstanding tool for researchers and software developers, while the wide range of available application software turns it into a powerful workbench for engineers.



# HARDWARE FEATURES

#### **Model Configuration**

#### **Desk-side System**

A SX-6i is a complete vector system consisting of one vector processor with 8 GFLOPS peak performance. The processor is connected to an uniform main memory with a capacity of 4 GB or 8 GB. The SX-6i comes in three different types of frames: One desk-side and two rack-mounted variants. The desk-side and the smaller rack-mounted version hold one processor configuration, while two systems can be installed in the larger rack. The SX-6i is binary compatible to the other SX-6 series systems, as well as to its predecessor the SX-5 series. It excels in the total balance of processing performance, memory bandwidth, input/output throughput in much the same way as the former SX series systems did. Existing applications and resources can be easily migrated to the SX-6i.

Larger systems of the SX-6 series are built from the SX-6 node, which holds up to eight processors and up to 64 GB of uniform shared main memory. These nodes may be clustered in order to form even bigger configurations: Up to 128 SX-6 nodes can be connected via NEC's proprietary Internode Crossbar Switch (IXS).

The NEC SX-6i in its desk-side chassis is the smallest vector supercomputer available today. The system holds one SX-6 processor and 8 GB or 4 GB of main memory. The I/O subsystem supports up six channels, which can be equipped with various interfaces for disks, network and other peripherals. The SX-6i desk-side chassis is available in several different colours. This system is the ideal tool for a researcher, an engineer or a software developer, who needs the power of a vector supercomputer and needs to utilize it freely and independently.

#### **Rack-mounted System**

The rack-mounted version of the SX-6i comes in two different sizes: The 25U rack can hold one SX-6i system and has more space for internal peripherals than the desk-side version. The 37U rackmount version holds two independent SX-6i systems. The processor and memory specifications are the same as for the desk-side version. Both rack-mounted variants are meant to serve in departmental or laboratory computer rooms.

The colourful super The desk-side case is available Ultrahigh-speed Vector and Scalar Unit

The vector unit of the SX-6 series processor consists of vector registers and eight sets of pipelines for logical operations, multiplication, add/shift operations, division, masked operations and load/store. The scalar unit realizes ultrahigh-speed performance through a four-way superscalar design. The integration of the processor in just on chip facilitates a reduced clock cycle when compared to the SX-5. This leads to a decreased processing time for each instruction and a superior performance on short vectors and scalar operations.

# **TECHNOLOGY**

#### The Worlds first Single-chip **Vector Processor**

The high gate density possible for the state-of-the-art CMOS technology and LSI design enabled NEC to implement the vector processor on just one chip. This LSI and packaging technology provides a performance of 8 GFLOPS on a single LSI. This ultrahigh integration leads to improved internal latencies and performance in comparison with former generation designs, which used dozens of chips to implement a processor, as well as highly reduced memory latencies by drastically narrowing the distance between memory and processors.

#### Main Memory Unit

The SX-6 series utilizes ultrahigh-speed double-data rate synchronous DRAM. SX-6i systems have a memory capacity of 4 GB or 8 GB and a memory bandwidth 32 GB per second. A large memory bandwidth turns out to be most important for the successful and fast solution of large-scale numerical problems.

ngineer's desk

Courtesy of HRLS. Stuttgart/Germany tion renault engi

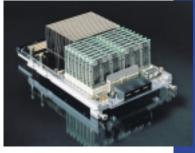


### Ease of Installation

The SX-6 series power consumption and space requirements have been reduced by 80% when compared with the previous generation of the SX series. The low power consumption allows all models to be fully air-cooled. These two elements contribute to a great reduction of installation costs and complexities, and they have allowed the development of the SX-6i as a desk-side system.

#### **High Reliability**

The usage of highly integrated CMOS technology has led to greatly reduced number of components in a single system. This, in turn, leads to a greatly improved hardware reliability.



SX-6 Memory module

